

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.	
				M-15372 US		Unassigned	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)			
(Use several sheets if necessary)				Kook-Hwan Kwon			
				Filing Date		Group	
				Filed Herewith		Unassigned	
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,011,737	4 Jan. 2000	Li et al.			
	AB	6,115,321	5 Sept. 2000	Koelling et al.			
	AC	6,285,578	4 Sept. 2001	Huang			
	AD	6,563,747	13 May 2003	Faue			
	AE	6,597,630	22 July 2003	Kubo et al.			
	AF	6,600,691	29 July 2003	Morzano et al.			
	AG	6,621,747	16 Sept. 2003	Faue			
	AH	2002/0149960	17 Oct. 2002	Yoo et al.			
	AI	2003/0026161	6 Feb. 2003	Yamaguchi et al.			
	AJ						
	AK						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AL	JEDEC Standard: DDR2 SDRAM Specification: JESD79-2A, January 2004, JEDEC Solid State Technology Association.					
	AM	"Preliminary Data Sheet: 1G bits DDR2 SDRAM: EDE1104AASE (256M words x4 bits): EDE1108AASE (128M words x 8bits); Elpida Memory, Inc. 2003.					
	AN	Davis, Brian; Mudge, Trevor; Jacob, Bruce; Cuppu, Vinodh "DDR2 and Low Latency Variants" Electrical Engineering and Computer Science, University of Michigan, Ann Arbor; Electrical & Computer Engineering, University of Maryland, College Park, pages 1-15.					
	AO	HYS64T32000GDL (256 Mbyte): HYS64R64020GDL (512 Mbyte) DDR2 Small Outline DIMM Modules; Data Sheet, Vol. 82, Oct. 2003, Infineon Technologies.					
	AP	"Preliminary Data Sheet: 512 M bits DDR SDRAM: EDD5104ABTA (128M words x 4 bits): EDD5108ABTA (64M words x 8 bits); Elpida Memory, Inc. 2002-2003.					
	AQ	JEDEC Standard: Double Data Rate (DDR) SDRAM Specification: JESD79D, January 2004, JEDEC Solid State Technology Association.					
	AR	United States Patent Application No. 10/794,782, entitled "Data Sorting in Memories," Filed on March 3, 2004; Attorney Docket No.: M-15341 US.					
Examiner			Date Considered				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	Unassigned	Filing Date:	Filed Herewith
Confirmation No.:	Unassigned		
First Named Inventor:	Kook-Hwan Kwon		
Assignee:	ProMOS Technologies, Inc.		
Examiner:	Unassigned	Art Unit:	Unassigned
Attorney Docket No.:	M-15372 US		

San Jose, California
April 2, 2004

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(B)

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patents and United States Published Patent Applications.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be material to patentability as defined in § 1.56(b).

No fee is believed to be required. If a fee is required for this Information Disclosure Statement, please charge the fee to Deposit Account No. 50-2257. This paper is being submitted in duplicate.

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Respectfully submitted,

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Application No. Unassigned